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Pedroni teaches synthesizable VHDL, the kind actually used by electronic design automation tools to make real circuits, and he manages to introduce the language, application, and software tool at the same time. The book is probably too basic for a practitioner wishing to brush up on the language or synthesis, but for the beginner it rocks.

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Integrated circuits design (CMOS ASICs), circuit synthesis with VHDL, FPGA-based design, hardware-implemented algorithms for control and communications applications. Website for books published by MIT Press: vhdl.us Contact: pedroni@utfpr.edu.br

Prof. Volnei A. Pedroni, UTFPR - Home

Part I, "Circuit Design," examines in detail the background and coding techniques of VHDL, including code structure, data types, operators and attributes, concurrent and sequential statements and code, objects (signals, variables and constants).

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