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VLSI Digital System Algorithms & Architectures. ELDON ECE 260B. VLSI Integrated Circuits & Systems Design KANG ECE 260C. VLSI Advanced Topics ELDON: ECE 264A. CMOS Analog Integrated Circuits & Systems I GALTON ECE 264B. CMOS Analog Integrated Circuits & Systems II GALTON: ECE 264C. CMOS Analog Integrated Circuits & Systems II I. GALTON ECE 264D

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Courses - Electrical and Computer Engineering

The scaling of silicon complementary metal-oxide-semiconductor (CMOS) technology has reached sub-10-nm technology nodes, but further scaling is increasingly challenging because the gate ...

Transistors based on two-dimensional materials for future integrated ...

Silicon photonics is the study and application of photonic systems which use silicon as an optical medium. The silicon is usually patterned with sub-micrometre precision, into microphotonic components. These operate in the infrared, most commonly at the 1.55 micrometre wavelength used by most fiber optic telecommunication systems. The silicon typically lies on top of a layer of silica in what ...

Silicon photonics - Wikipedia

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Research Interests: Design and fabrication of microsensors, microactuators, and micro-electro-mechanical systems (MEMS) for a variety of applications such as inertial sensing, environmental sensing, scanning microscopy, microfluidics, microoptics, and biomedical instrumentation; Development of manufacturing processes using combinations of traditional and novel materials and techniques, for ...

All ECE Faculty | Electrical & Computer Engineering at Michigan

Mixed analog-digital neuromorphic approaches have built large-scale systems (6–8) to emulate neurobiology by using custom computational elements, for example, silicon neurons (9, 10), winner-take-all circuits , and sensory circuits . We have found that a multiplexed digital implementation of spiking neurons is more efficient than previous designs (supplementary section S3) and enables one-to ...

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A million spiking-neuron integrated circuit with a scalable ...

On-chip sensing and spectroscopy: capitalizing on a digital Fourier-Transform (dFT) ... M. Kang et al., "Advances in ... in Silicon Photonics and Photonic Integrated Circuits V, vol. 9891, Bellingham: Spie-Int Soc Optical Engineering, 2016, p. UNSP - 98911J. D. Kita et al., "Suspended chalcogenide microcavities for ultra-sensitive chemical detection", in 2016 IEEE Sensors, New York: IEEE ...

Juejun (JJ) Hu | MIT DMSE

From 1985 to 1992, he was a member of technical staff at Telcordia, where he was involved in transport and switching system architecture designs and Application-specific Integrated Circuits (ASIC) implementations. He was a senior engineer at Telecommunication Labs of Taiwan performing circuit designs for a digital telephone switching system from 1977 to 1981.

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H. Jonathan Chao | NYU Tandon School of Engineering

System on Panel, SoP

pku.edu.cn

In digital circuits, ... (FET) technologies comparing complementary metal-oxide semiconductor (CMOS) high-performance and CMOS low-voltage in the energy-delay product metric for digital logic ...

Two-dimensional semiconductors for transistors - Nature

Digital Orthogonal Filtering-Enabled Synchronous Transmissions of I/Q Waveforms and Control Words for Bandwidth-Efficient and Low-Complexity Mobile Fronthaul . M. L. Deng, T. Mamadou, Z. B. Xing, X. Kang, Z. R. Luo, J. W. Shi, and L. Wang. F1D.2 Optical

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Fiber Communication Conference (OFC)
2021 View: PDF. Novel Scalable and
Reconfigurable Optical Fronthaul
Network for Converged Radio Frequency

...

Optical Fiber Communication Conference 2021 - OSA

In semiconductor physics, the depletion region, also called depletion layer, depletion zone, junction region, space charge region or space charge layer, is an insulating region within a conductive, doped semiconductor material where the mobile charge carriers have been diffused away, or have been forced away by an electric field. The only elements left in the depletion region are ionized donor ...

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