

Digital Logic Design Donald Givone Weeksy

This book is evolved from the experience of the author who taught all lab courses in his three decades of teaching in various universities in India. The objective of this lab manual is to provide information to undergraduate students to practice experiments in electronics laboratories. This book covers 118 experiments for linear/analog integrated circuits lab, communication engineering lab, power electronics lab, microwave lab and optical communication lab. The experiments described in this book enable the students to learn:

- Various analog integrated circuits and their functions
- Analog and digital communication techniques
- Power electronics circuits and their functions
- Microwave equipment and components
- Optical communication devices

This book is intended for the B.Tech students of Electronics and Communication Engineering, Electrical and Electronics Engineering, Biomedical Electronics, Instrumentation and Control, Computer Science, and Applied Electronics. It is designed not only for engineering students, but can also be used by BSc/MSc (Physics) and Diploma students.

KEY FEATURES

- Contains aim, components and equipment required, theory, circuit diagram, pin-outs of active devices, design, tables, graphs, alternate circuits, and troubleshooting techniques for each experiment
- Includes viva voce and examination questions with their answers
- Provides exposure on various devices

TARGET AUDIENCE

- B.Tech (Electronics and Communication Engineering, Electrical and Electronics Engineering, Biomedical Electronics, Instrumentation and Control, Computer Science, and Applied Electronics)
- BSc/MSc (Physics)
- Diploma (Engineering)

I see it all the time: Businesses implement the latest Agile and DevOps practices from the software industry, hoping that simply doing so will provide the required improvements. But months and even years later, they're still struggling. Similarly, I watch the software industry trying to learn and implement wholesale what the manufacturing industry did years ago. As it turns out, we can't just copy what others have done. Businesses need to understand the unique challenges of their company. And digital assets like software are very different from physical assets that are manufactured. Every business is different, and software development is different from manufacturing. *Engineering the Digital Transformation* provides systematic approaches to improving how software is developed for a broad range of applications. This book focuses on high-level principles for engineering improvements, leveraging as much as possible from manufacturing, and modifies them to address the unique characteristics and capabilities of software.

Learn FileMaker® Pro 10 provides an excellent reference to FileMaker Inc.'s award-winning database program for both beginners and advanced developers. From converting files created with previous versions of FileMaker Pro and sharing data on the web to creating reports and sorting data, this book offers a hands-on approach to getting the most out of your FileMaker Pro databases. Learn how to use the completely redesigned Status area, now known as the Status toolbar; send e-mail right from FileMaker with the SMTP-based Send Mail option; build reports quickly and easily with the Saved Finds feature; automate your database with scripts and activate those scripts with the new script trigger feature; integrate your Bento data into your FileMaker files; work with the enhanced Web viewer.

Where To Download Digital Logic Design Donald Givone Weeksy

DIGITAL LOGIC offers the right balance of classical and up-to-date treatment of combinational and sequential logic design for a first digital logic design class. The author provides a thorough explanation of the design process, including completely worked examples beginning with simple examples and going on to problems of increasing complexity. This text contains PLD (Programmable Logic Design) coverage. Chapter 9 develops complete, worked EPROM, PLA, and EPLD design examples. The problems are developed in Chapter 7 as standard designs using SSI and MSI devices so that your students can see the difference between the two approaches.

Explains Fundamentals of Digital Computers & Operation of Microprocessors Through a Hypothetical Model of a Microcomputer. Provides Problems after Each Chapter

The fundamentals and implementation of digital electronics are essential to understanding the design and working of consumer/industrial electronics, communications, embedded systems, computers, security and military equipment. Devices used in applications such as these are constantly decreasing in size and employing more complex technology. It is therefore essential for engineers and students to understand the fundamentals, implementation and application principles of digital electronics, devices and integrated circuits. This is so that they can use the most appropriate and effective technique to suit their technical need. This book provides practical and comprehensive coverage of digital electronics, bringing together information on fundamental theory, operational aspects and potential applications. With worked problems, examples, and review questions for each chapter, Digital Electronics includes: information on number systems, binary codes, digital arithmetic, logic gates and families, and Boolean algebra; an in-depth look at multiplexers, de-multiplexers, devices for arithmetic operations, flip-flops and related devices, counters and registers, and data conversion circuits; up-to-date coverage of recent application fields, such as programmable logic devices, microprocessors, microcontrollers, digital troubleshooting and digital instrumentation. A comprehensive, must-read book on digital electronics for senior undergraduate and graduate students of electrical, electronics and computer engineering, and a valuable reference book for professionals and researchers.

This is a collection of invited papers from the 1975 International Symposium on Multiple-valued Logic. Also included is an extensive bibliography of works in the field of multiple-valued logic prior to 1975 - this supplements and extends an earlier bibliography of works prior to 1965, by Nicholas Rescher in his book *Many-Valued Logic*, McGraw-Hill, 1969. There are a number of possible reasons for interest in the present volume. First, the range of various uses covered in this collection of papers may be taken as indicative of a breadth which occurs in the field of multiple-valued logic as a whole - the papers here can do no more than cover a small sample: question-answering systems, analysis of computer hazards, algebraic structures relating to multiple-valued logic, algebra of computer programs, fuzzy sets. Second, a large part of the interest in such uses and applications has occurred in the last twenty, even ten years. It would be too much to expect this to be reflected in Rescher's 1969 book. Third, in the 1970's a series of annual symposia have been held on multiple-valued logic, which have brought much of this into a sharp focus. * The 1971 and 1972 symposia were held at the SUNY at Buffalo, the 1973 symposium at the University of Toronto, and the 1974 symposium at West Virginia University. Papers from these symposia are included in the bibliography which may be found in an appendix of this book.

This is the only book on the market that has been conceived and deliberately written as a one-semester text on basic electric circuit theory. As such, this book employs a novel approach to the exposition of the material in which phasors and ac steady-state analysis are introduced

authors have carefully developed a clear presentation that introduces the fundamental concepts of logic design without overwhelming you with the mathematics of switching theory. Twenty engaging, easy-to-follow study units present basic concepts, such as Boolean algebra, logic gate design, flip-flops and state machines. You learn to design counters, adders, sequence detectors and simple digital systems. After mastering the basics, you progress to modern design techniques using programmable logic devices as well as VHDL hardware description language. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

In two editions spanning more than a decade, The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering. Our knowledge continues to grow, and so does the Handbook. For the third edition, it has expanded into a set of six books carefully focused on a specialized area or field of study. Each book represents a concise yet definitive collection of key concepts, models, and equations in its respective domain, thoughtfully gathered for convenient access. Computers, Software Engineering, and Digital Devices examines digital and logical devices, displays, testing, software, and computers, presenting the fundamental concepts needed to ensure a thorough understanding of each field. It treats the emerging fields of programmable logic, hardware description languages, and parallel computing in detail. Each article includes defining terms, references, and sources of further information. Encompassing the work of the world's foremost experts in their respective specialties, Computers, Software Engineering, and Digital Devices features the latest developments, the broadest scope of coverage, and new material on secure electronic commerce and parallel computing.

Digital Principles and Design Palgrave Macmillan Digital Principles & Logic Design Jones & Bartlett Learning

Over 220,000 entries representing some 56,000 Library of Congress subject headings. Covers all disciplines of science and technology, e.g., engineering, agriculture, and domestic arts. Also contains at least 5000 titles published before 1876. Has many applications in libraries, information centers, and other organizations concerned with scientific and technological literature. Subject index contains main listing of entries. Each entry gives cataloging as prepared by the Library of Congress. Author/title indexes. mental improvements during the same period. What is clearly needed in verification techniques and technology is the equivalent of a synthesis productivity breakthrough. In the second edition of Writing Testbenches, Bergeron raises the verification level of abstraction by introducing coverage-driven constrained-random transaction-level self-checking testbenches all made possible through the introduction of hardware verification languages (HVLs), such as e from Verisity and OpenVera from Synopsys. The state-of-art methodologies described in Writing Test benches will contribute greatly to the much-needed equivalent of a synthesis breakthrough in verification productivity. I not only highly recommend this book, but also I think it should be required reading by anyone involved in design and verification of today's ASIC, SoCs and systems. Harry Foster Chief Architect Verplex Systems, Inc. xviii Writing Testbenches: Functional Verification of HDL Models PREFACE If you survey hardware design groups, you will learn that between 60% and 80% of their effort is now dedicated to verification.

Computer Science and Multiple-Valued Logic: Theory and Applications focuses on the processes, methodologies, and approaches

involved in multiple-valued logic and its relationship to computer science. The selection first tackles an introduction to multiple-valued logic, lattice theory of post algebras, multiple-valued logic design and applications in binary computers, smallest many-valued logic for the treatment of complemented and uncomplemented error signals, and chain based lattices. Discussions focus on formulation, representation theory, theory and circuit design, logical tables, and unary operations. The text then examines multiple-valued signal processing with limiting, development of multiple-valued logic as related to computer science, p-algebras, and an algorithm for axiomatizing every finite logic. The book takes a look at completeness properties of multiple-valued logic algebras, computer simplification of multi-valued switching functions, and minimization of multivalued functions. Topics include generation of prime implicants, realizations, minimization algorithms, decomposition algorithm for multi-valued switching functions, and relation between the sum-of-products form and array of cubes. The selection is aimed at computer engineers, computer scientists, applied mathematicians, and physicists interested in multiple-valued logic as the discipline relates to computer engineering and computer science.

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